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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Carns et al.

Assignee: ZiLOG, Inc.

Title: "Process to Improve High Performance Capacitor Properties in Integrated MOS Technologies"

Appl. No.: 09/351,544

Filing Date: July 12, 1999

Examiner: Paul E. Brock II

TC/Art Unit: 2815

Docket No.: ZIL-204

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April 20, 2004

**NOTICE OF APPEAL FROM THE PRIMARY
EXAMINER TO THE BOARD OF PATENT
APPEALS AND INTERFERENCES**

Applicants hereby appeal to the Board of Patent Appeals and Interferences from the decision of the Primary Examiner dated January 20, 2004 finally rejecting claims 3-11, 36-39, 72-74 and 102-108 of the above-referenced application. Claims 109 – 114 were withdrawn from consideration by the Examiner and not the subject of this appeal.

A check is enclosed in the amount of \$330 for this Notice of Appeal are required under 37 CFR §1.17(b).

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Mail Stop AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

By Darien K. Wallace
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Date of Deposit: April 20, 2004

Respectfully submitted

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